



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Derderian et al.

Serial No.: 10/690,339

Filed: October 21, 2003

For: THINNED, STRENGTHENED
SEMICONDUCTOR SUBSTRATES

Confirmation No.: 2183

Examiner: D. Le

Group Art Unit: 2818

Attorney Docket No.: 2269-5713US
(02-1473.00/US)

Notice of Allowance Mailed:

April 1, 2005

Express Mail Mailing Label No.: EL 994849098 US

Date of Deposit with USPS: June 30, 2005

Person making Deposit: Steve Wong

TRANSMITTAL LETTER

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

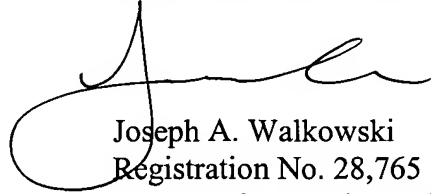
Applicants submit herewith Part B - Fee(s) Transmittal for the above-captioned application and a check in the amount of \$1,715.00 in payment therefor plus five (5) copies of the patent when issued.

Also enclosed is an Amendment Pursuant to 37 C.F.R. § 1.312(a) (25 pages), plus attached Replacement Sheet of Drawings (1 sheet) and Annotated Sheet Showing Changes Made

(1 sheet); Comments on Statement of Reasons for Allowance (2 pages); and Fee Addressee for Receipt of PTO Notices Relating to Maintenance Fees (2 pages).

Applicants understand that no additional fees are required. However, if the Office determines that any comparison fees or other additional fees are required, the Commissioner is authorized to charge any such fees to TraskBritt Deposit Account No. 20-1469. A copy of this Transmittal Letter is enclosed for deposit account charging purposes.

Respectfully submitted,



Joseph A. Walkowski
Registration No. 28,765
Attorney for Applicant(s)
TRASKBRITT
P.O. Box 2550
Salt Lake City, Utah 84110-2550
Telephone: 801-532-1922

Date: June 30, 2005
JAW/nj:sm

Enclosures: Part B - Issue Fee Transmittal
Check No. 21870 in the amount of \$1,715.00
Copy of Transmittal Letter
Amendment Pursuant to 37 C.F.R. § 1.312(a) (25 pages)
Attached Replacement Sheet of Drawings (1 sheet) and Annotated Sheet Showing Changes Made (1 sheet)
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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

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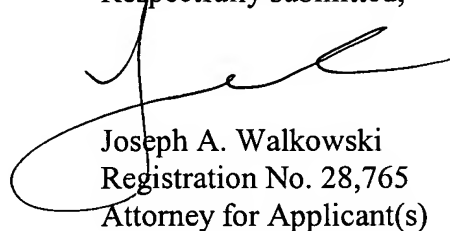
Sir:

The Examiner indicates:

None of the references of record teaches or suggests the claimed Semiconductor package having an interface between the backside surface and the solid material has a mean surface roughness factor Ra of between about 5% to about 40% of the mean thickness of the die and among other limitations as cited in independent claims.

Applicants concur with the reasons as stated by the Examiner insofar as they comprise a summary, and are exemplary and not limiting. However, the independent claims as allowed include other and different language than that specified by the Examiner, and the allowed dependent claims include other and further features and elements. Accordingly, the scope of the claims must be determined from the literal language of each as a whole, as well as equivalents thereof.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'J. Walkowski', is written over the typed name and registration number.

Joseph A. Walkowski
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Date: June 30, 2005
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Document in ProLaw